







A Report on : "R&D TALK"

Event Date : 23<sup>TH</sup>-AUGUST-2023

Venue : D2, ,CKPCET

Organised by: IEEE SPS CKPCET SBC collaboration with ECE Department CKPCET

**Coordinator**: VIVEK PAL

## **BIOGRAPHY OF SPEAKER:**

-NAME: RAJ VIJAY ICECREAMWALA

-Design manager at True chip and Alumni student of electronics & communication department (2015)



## **EVENT TALK:**

This event was organized with the aim to make students aware of the Importance of Hardware in current and upcoming generation. The speaker also explain about in VLSI field. How many opportunities are available for them. Which type of preparation they need to do. The speaker has not only covered the above points but also gives the idea about the important subjects of course

structure of electronics engineering. He interacted with many students in questions and answer sessions where students have asked their practical difficulties and the speaker guided each of them in detail.





It was a wonderful experience to have an Alumni student as a speaker and addressing our present and recently passed out students. The strength of attendees are approximately 25. All the students of second to final year have taken part in this webinar.



**REPORT MAKE BY: VIVEK PAL & HEMANSHI DHANANI**