



A Report on : SEMICONDUCTOR SIPS WITH VIDHI GANDHI

Event Date : 30TH-SEPTEMBER-2023

Venue : D2, SEMINAR HALL ,CKPCET

Organised by : IEEE SPS CKPCET SBC collaboration with ECE Department CKPCET

Coordinator : VIVEK PAL



Navyug Vidya Bhavan Trust
C.K.Pithawala College of Engg & Tech
Electronics & Communication Department



SEMICONDUCTOR SIPS

WITH **Vidhi Gandhi**

SATURDAY
30 September 2023

TIME
11:00 AM Onward

SPEAKER
Vidhi Gandhi
Alumni ECED
(2021)

Student Co-ordinator
Vivek Pal

Faculty Co-ordinator
Dr. Mita Paunwala
(Dean R & C)
Dr. Amisha Shah
(HOD, ECC)



BIOGRAPHY OF SPEAKER:

She has completed her bachelor form C.K PITHAWALA COLLEGE OF ENGINEERING AND TECHNOLOGY, and currently she completing master from GANPAT UNIVERSITY and as well working in ELNFOCHIPS as a fresher.

SUMMARY OF EVENT:

In the starting DR. Amish Shah (HOD, ECE DEPARTMENT) Greeting to the speaker and introduced about her. She start to share her experience. The speaker share our experience related to in VLSI domain for chip design. How many opportunities are available for them in such sector. The speaker are not only covered the above point but also give the idea which kind of stages are there and explain each stages individually in which the stage one is design, second is physical application, and third is testing such kind of stage are following for chip design. And also give the idea how we can enter in the good and multinational company, as well as which subject course structure is more important.



She interacted with many students in questions and answer sessions where students have asked their practical difficulties and the speaker guide each of them in detail. It was a wonderful experience to have a Alumni student as a speaker. The strength of attendance are around 20 in which the students of second to third year have take part in this webinar.



REPORT PREPARED BY: VIVEK PAL & HEMANSHI DHANANI